

## Nanosimulator for Analysis of Mosfet at Nanoscale

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**Abstract** - At nanoscale dimensions (below 100nm) the behavioral characteristics of MOSFET change due to quantum mechanical effect and deviate from the normal equations. In this paper we have proposed a simple Graphical user interface model-NANO SIMULATOR . This simulator is based upon modified equations to predict the curves of nanoscale MOSFETs. It has been developed to study the Current-Voltage characteristics and the model parameters i.e. threshold voltage, flat band voltage, mobility of nanoscale MOSFETs specifically at 90nm, 65nm, 45nm, 32nm technology nodes (or CMOS Process). This GUI based NANO SIMULATOR also helps in studying various types of leakage currents at 90nm, 65nm, 45nm, 32nm technology nodes.

**Keywords** - I-V Curve, Leakage Current Curve, Pushbutton, popup menu, MatLab GUI

### I. NOMENCLATURE

V<sub>gs</sub>- Gate to Source voltage  
V<sub>ds</sub>- Drain to Source voltage  
V<sub>th</sub>- Threshold voltage  
V<sub>fb</sub>- Flat band voltage  
J<sub>tn</sub>- Direct gate tunneling in n-channel MOSFET current density  
J<sub>sdn</sub>- Source Drain Extension tunneling current density  
J<sub>tpoly</sub>-Leakage current density due to poly gate  
J<sub>fn</sub>- Fowler Nordheim Tunneling  
E<sub>ox</sub>- Dielectric constant of gate oxide  
EOT- Equivalent Oxide Thickness  
C<sub>ox</sub> - Oxide capacitance  
 $\mu$  - Mobility

### II. INTRODUCTION

As the transistor gate length drops to 45 nm and below and the gate oxide thickness drops to 1 nm physical limitations, such as off-state leakage current and reduction in drive currents, make geometric scaling an increasingly challenging task. Moreover, a combined study of I-V Curves on the basis of channel length scaling along with the leakage currents on the basis of gate oxide thickness scaling at various

technology nodes by a GUI-SIMULATOR has not been reported yet in the existing literature. In this paper, we present our GUI-SIMULATOR to study MOSFET at 90nm, 65nm and 45nm technology nodes. This simulator is built upon MATLAB-*Graphical user interface* and the curves which this simulator will provide are also calculated by MATLAB Tool.

MatLab software environment with its own Graphical User Interface (GUI). The GUI is used to analyze the I-V characteristics of MOSFET at nanoscale and displays the curves and parameters such that a user can visualize the voltage and current variations as well as different type of leakage currents. The user interface also plays a vital role in controlling the application's performance to suit the user's requirements. The objective of this work is to make a easy understanding of MOSFET at nanoscale using a simple simulator with simple MOSFET equations. This simulator has the ability to take the input from user, and generates the characteristic curve and corresponding parameters. This paper is organized as follows. In section III, NANOSIMULATOR's structure is shown by flow diagram in fig.1. Section IV describes nanoscale MOSFET modeling equations. Section V, explains how we can install the simulator. Section VI, explains modeling results for leakage currents. Section VII, describes modeling results for input-output characteristics. A summary and future work is presented in section VIII.

III. SOFTWARE STRUCTURE

The proposed simulator is built in MatLab software environment with its own GUI. MatLab code here used is in 'm-file' at backend because it is more flexible and have fewer limitations. User end code is in ".pfile", so that our original file can be saved from unwanted disturbances. The GUI is a user friendly interface that can provide a user with the access to perform I-V analysis of MOSFET. This simulator can perform the following processing:

- Id-Vds Curve
- Id-Vgs Curve
- Jtn-Vgs Curve
- Jsdn-Vgs Curve
- Jtpoly-Vgs Curve
- Jfn-Vgs Curve
- Jfn-Eox Curve
- Jtn-EOT Curve
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Fig. 1 shows the flowchart of the simulator, where the code has been written using MatLab code to obtain the results on the GUI.

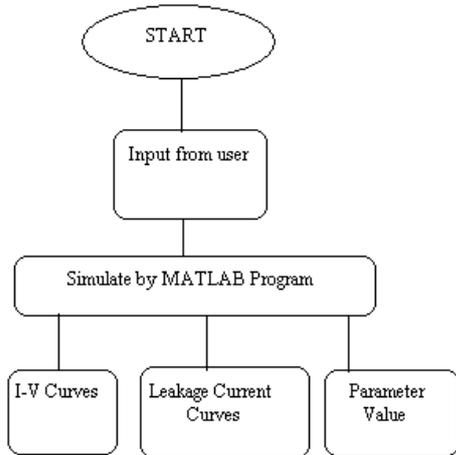


Fig. 1 Flowchart of the proposed simulator

IV. NANOSCALE MOSFET MODEL

Here we are giving analytical MOSFET's I-V model for circuit simulator. This approach is necessary for circuit design. Analytical model is of two types[4], one is physically based, the other is continuity between different operation regions. Since Quantum Mechanical Effects have significant influences on the threshold voltages[2], on gate capacitance attenuation[3] due to finite inversion layer thickness.

**Modified equation for Threshold voltage:**

Initially the threshold voltage will be equation 1

$$V_{th} = V_{fb} + \phi_{s\_qm} + \sqrt{2 * q * \epsilon_{si} * N_a * \phi_{s\_qm}} / C_{ox}; \tag{1}$$

Threshold voltage in linear region due to QME is given by equation 2

$$V_{th\_qm\_linear} = V_{fb} + \phi_{s\_qm} + \phi_{c\_linear} + \sqrt{2 * q * \epsilon_{si} * N_a * (\phi_{c\_linear} + \phi_{s\_qm})} / C_{ox}; \tag{2}$$

Threshold voltage in saturation region due to QME is given by equation 3

$$V_{th\_qm\_sat} = V_{fb} + \phi_{s\_qm} + \phi_{c\_sat} + \sqrt{2 * q * \epsilon_{si} * N_a * (\phi_{c\_sat} + \phi_{s\_qm})} / C_{ox}; \tag{3}$$

Where

$$\alpha_t = ((3 * m_e * q^2 * N_a * d_{cl}) / (2 * \epsilon_{si} * h^2))^{1/3};$$

$$\phi_{s} = (q * N_a * (d_{cl})^2) / (2 * \epsilon_{si});$$

$$\phi_{s\_qm} = (q * N_a * ((d_{cl} + 9 / (4 * \alpha_t)^2)) / (2 * \epsilon_{si}));$$

$$V_{fb} = -0.56 * \phi_b; \text{ Flat Band Voltage}$$

$$\phi_{c\_linear} = (V_{dsat}) / 2;$$

$$\phi_{c\_sat} = V_{dsat} / 2;$$

**Modified equation for mobility:**

Due to QME and Short Channel Effects mobility equations are different from classical mobility.

Mobility in linear region is given by equation 4

$$u_{eff} = u_o / (1 + \theta * (V_{gs} - V_{th\_qm\_linear})); \tag{4}$$

Mobility in saturation region is given by equation 5

$$u_{eff\_sat} = u_o / (1 + \theta * (V_{gs} - V_{th\_qm\_sat})); \tag{5}$$

where

$$u_o = 0.06; \text{ low field surface electron mobility}$$

$$E_{cro\_linear} = v_{sat} / u_o; \text{ Vertical electric field component in linear region}$$

$$E_{cro\_sat} = v_{sat} / u_{eff\_sat}; \text{ Vertical electric field component in saturation region}$$

$$\theta = u_o / (2 * t_{ox} * v_{norm}); \tag{constant}$$

$$v_{norm} = 2.2e7; v_{sat} = 1e5; \tag{unit: m/s}$$

$v_{sat}$  is saturation velocity

**Modified equation for Bulk Depletion region charge:**

Without QME and SCE the value of  $Q_{BO}$  is given by equation6

$$Q_{dep} = q \cdot N_a \cdot d_{cl} \quad (6)$$

Without QME and SCE the value of  $Q_{BO}$  is given by equation7

$$Q_{bo\_qm} = q \cdot N_a \cdot (d_{cl} + (9/(4 \cdot \alpha_t))) \quad (7)$$

Where

$$d_{cl} = \sqrt{(2 \cdot \epsilon_{si} \cdot \phi_b) / (q \cdot N_a)}$$

$$\alpha_t = ((3 \cdot m_e \cdot q^2 \cdot N_a \cdot d_{cl}) / (2 \cdot \epsilon_{si} \cdot h^2))^{1/3}$$

$$h = 6.626 \cdot 10^{-34}; \quad \% \text{unit: J}\cdot\text{s}$$

$$h = h / (2 \cdot \pi);$$

$$\epsilon_o = 8.854 \cdot 10^{-12}; \quad \% \text{unit: F/m}$$

$$\epsilon_{si} = 11.80 \cdot \epsilon_o;$$

$$\epsilon_{sio2} = 3.90 \cdot \epsilon_o;$$

**Modified equation for  $I_{DS}$  and  $V_{dssat}$ :**

$$V_{dsat} = (L \cdot E_{cro\_linear} \cdot ((\sqrt{1 + (2 \cdot (V_{gs} - V_{th})) / (L \cdot E_{cro\_linear} \cdot \eta))} - 1));$$

$$I_{dst} = (W \cdot u_{eff} \cdot C_{t/L}) \cdot (1 / I_2) \cdot I_6;$$

$$I_{dsat} = ((W \cdot u_{eff} \cdot C_{t/L}) \cdot (1 / I_{ss1})) \cdot I_{s6};$$

Where

$$\eta = 1 - ((Q_{bo\_qm}) / (2 \cdot \phi_s \cdot q \cdot C_{ox}))$$

$$C_{t/L} = (1 / C_{ox} + 1 / C_{inv})^{-1}$$

$C_{inv}$  is capacitance of inversion layer

**Modified equation for  $J_{tn}$  and  $V_{GS}$ :**

$$A = \exp(-\gamma \cdot \sqrt{q \cdot E_b});$$

$$B = \exp(q \cdot (\phi_s - \phi_b - (E_g \cdot q) / (2 \cdot (k \cdot T)));$$

$$C = 1 - \exp(-V_{gs} / (k \cdot T));$$

$$E = 1 + (\gamma \cdot k \cdot T) / (2 \cdot \sqrt{q \cdot E_b});$$

$$F = 4 \cdot \pi \cdot q \cdot m_e \cdot ((k \cdot T)^2) / (h^3);$$

$$J_{t_n} = (F \cdot E \cdot C \cdot B \cdot A) \cdot 10^{-4};$$

Where

$$m_o = 9.11 \cdot 10^{-31} \quad \% \text{unit: Kg}$$

$$m_{ox} = 0.32 \cdot m_o;$$

$$m_e = 0.19 \cdot m_o;$$

$$h = 6.63 \cdot 10^{-34} \quad \% \text{unit: J}\cdot\text{s}$$

$$q = 1.6 \cdot 10^{-19} \quad \% \text{unit: C}$$

$$\phi_b = ((k \cdot T) / q) \cdot \log(N_a / n_i); \quad \% \text{eV}$$

$$\gamma = (4 \cdot \pi \cdot t_{ox} \cdot 10^{-9} \cdot \sqrt{2 \cdot m_{ox}}) / h;$$

$$N_a = 4 \cdot 10^{24} \quad \% \text{unit: m}^{-3}$$

$$n_i = 1.45 \cdot 10^{16} \quad \% \text{unit: m}^{-3}$$

$$k = 1.38 \cdot 10^{-23} \quad \% \text{unit: J/K}$$

$$T = 300 \quad \% \text{unit: K}$$

**Modified equation for  $J_{tpoly}$  and  $V_{GS}$ :**

$$A = \exp(-\gamma \cdot \sqrt{q \cdot E_b});$$

$$B = \exp((\phi_s - \phi_b - (E_g \cdot q) / (2 \cdot (k \cdot T)));$$

$$C = 1 - \exp(-V_{gs} / (k \cdot T));$$

$$E = 1 + (\gamma \cdot k \cdot T) / (2 \cdot \sqrt{q \cdot E_b});$$

$$G = (k \cdot T)^2;$$

$$F = 4 \cdot \pi \cdot m_e \cdot q^2 / (h^3);$$

$$J_{tpoly} = F \cdot G \cdot E \cdot C \cdot B \cdot A;$$

**Modified equation for  $J_{fn}$  and  $V_{GS}$ :**

$$A = (q^3 \cdot m_n \cdot E_{ox})^2;$$

$$B = 16 \cdot \pi^2 \cdot m_o \cdot h \cdot \phi_{ox}$$

$$C = 4 \cdot \sqrt{2 \cdot m_e} \cdot (\phi_{ox})^{3/2}$$

$$D = 3 \cdot h \cdot q \cdot E_{ox}$$

$$J_{fn} = ((A / B) \cdot \exp(-C / D)) \cdot 10^{-4};$$

Where

$$\phi_{ox} = q \cdot 3.1 \cdot \phi_b;$$

$$m_o = 9.11 \cdot 10^{-31}; \quad \% \text{unit: Kg}$$

$$m_e = 0.4 \cdot m_o;$$

$$m_n = 0.19 \cdot m_o;$$

$$V_{ox} = (V_{gs} - V_{fb}) - \phi_b;$$

$$E_{ox} = V_{ox} / (t_{ox} \cdot 10^{-9});$$

$$E_g = 1.12; \quad \% \text{unit: eV}$$

**V. HOW TO INSTAL NANOSIMULATOR**

The display results are obtained using the proposed MatLab GUI. Fig. 3 is the MatLab GUI window that shows the input from the user, analysis of characteristic curves, parameter analysis. Copy two files from the given CD namely NanoSim.p and NanoSim.fig .Paste these files in a particular directory and set the path of the MatLabDirectory. Type “run NanoSim” on Command Window of MATLAB Press ‘Enter’ and the simulator will be displayed as shown.

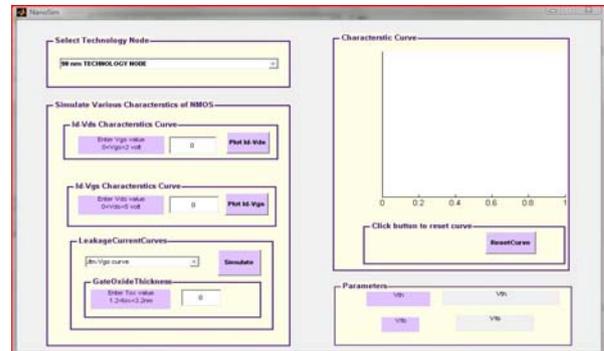


Fig. 2 Nano Simulator

VI .MODELING RESULTS FOR LEAKAGE CURRENTS

An analytical model for gate direct tunneling current density is modeled using MATLAB codes, the results drawn are being presented here.

A)Gate Direct Tunneling Current Density in n-MOSFET

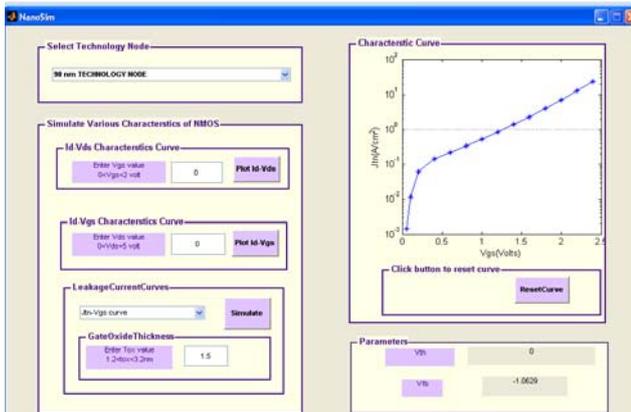


Fig 3.Tunneling Current Density  $J_t$ (A/cm<sup>2</sup>) versus  $V_{gs}$  (Volts) in n-MOSFET at Various Oxide Thicknesses(i.e.  $t_{ox}$ =1.5nm)[6]

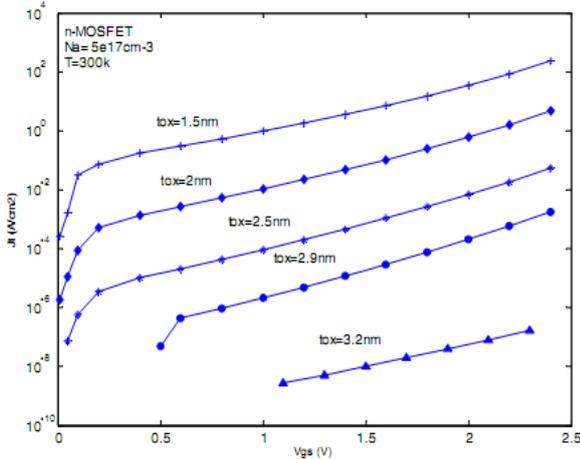


Fig 4: Plot of fig3 is matching with ref[6]

B)Edge Direct Tunneling Current Density

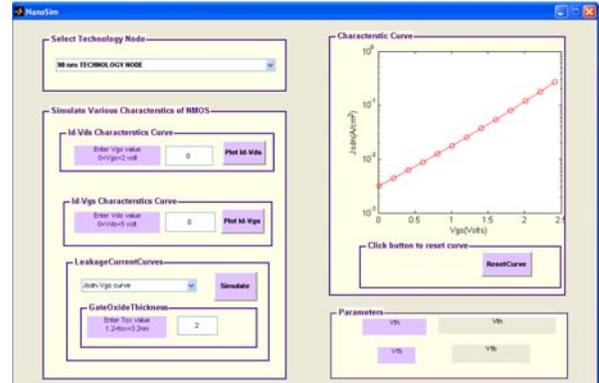


Fig 5.SDE Tunneling Currents in n-MOSFET at 2nm Oxide Thickness[6]

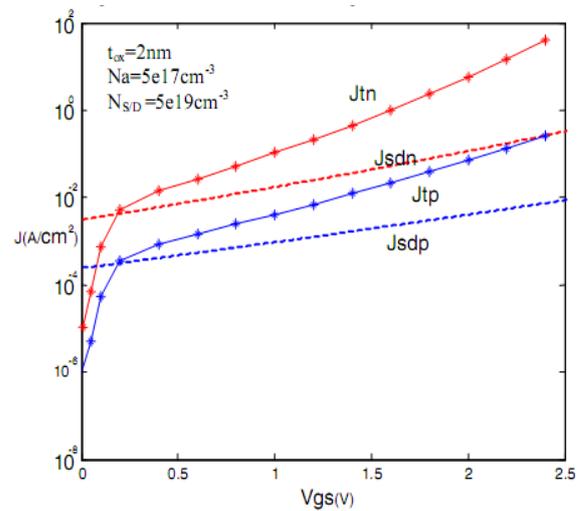


Fig 6: Plot of fig5 is matching with ref[6]

VII .MODELING RESULTS FOR I/O CHARACTERSTICS

$V_{th}$ ,  $C_{ox}$ ,  $\mu$  are the parameters which undergo a change due to Short Channel Effects and so the conventional equations are invalid at Nanoscale and we include Quantum Mechanical effects[1]. And by the addition of QME effects we got such input-output characteristic curves as shown in Fig7&Fig8 at 90nm technology node.

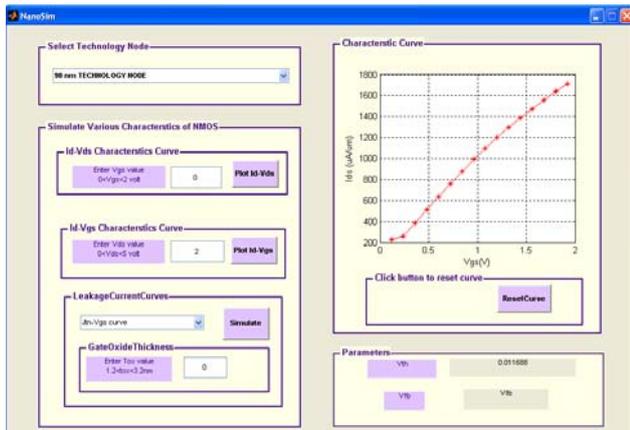


Fig7: Id-Vgs Curve

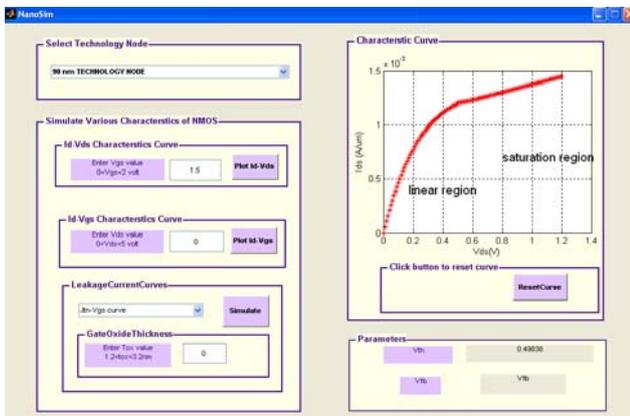


Fig8: Id-Vds Curve at Vgs=1.5v [5]

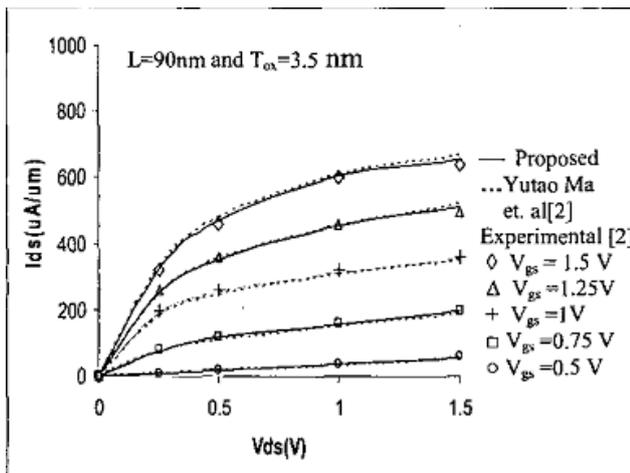


Fig9: : Plot of fig8 is matching with ref[5]

## VIII. CONCLUSION

A simulator for Leakage Current detection and classification as well as I-V characteristics using MatLab program has been developed. At all technology nodes I-V Curves were successfully visualized. The Leakage Current detection, Input-Output characteristic results are displayed to a user via MatLab graphical user interface (GUI). The significant features of the developed GUI are the ability to graphically display the results by taking inputs from user side, hence this tool is user friendly. At backend we have done calculation by considering short channel effects and QME effects hence the developed simulator provides accurate, efficient and reliable curves and parameter values to study. In future this simulator will be available to our users online with some special functions like online help and also more curves can be studied by this (i.e. Doping and Temperature effects on nanoscale mosfet).

## IX. REFERENCES

- [1] J. Mracek, "Understanding Quantum Mechanical Tunneling in MOSFET Gate Oxides", Journal of Physics 334, Article no. 0017, Dec 2007.
- [2] Tomasz Janik, Bogdan Majkusiak. "Influence of carrier energy quantization on threshold voltage of metal-oxide-semiconductor transistor", J.Appl.Phys. ~0175, nolo, pp5 186-5 190, 1994.
- [3] R. Versari, B. Ricco . "Scaling of maximum capacitance of MOSFET with ultra-thin oxide." Electronics Letters v34, n22, p 2175-2176, 1998.
- [4] Yutao Ma, Litian Liu, Lilin Tian, Zhiping Yu\* and Zhijian Li "Comprehensive Analytical Charge Control and I-V Model of Modern MOSFET's by Fully Comprising Quantum Mechanical Effects" 0-7803-6279-9/00/\$10.00 0 2000 IEEE
- [5] Manisha Pattanaik and Swapna Banerjee "A New Approach to Model the I-V Characteristics for Nanoscale MOSFETs" 0-7803-7765-6/03/\$17.00 02003 IEEE.
- [6] Garima Joshi , D.N. Singh, Member IEEE, Sharmelee Thangjam "Effect of Temperature Variation on Gate Tunneling Currents in Nanoscale MOSFETs" Nanotechnology, 2008. NANO '08. 8th IEEE Conference on.

BIOGRAPHIES

**Er. Pragya Kushwaha** was born in 1988. She is a student of M.Tech Microelectronics U.I.E.T(P.U.) Chandigarh, India. She obtained her Bachelor Degree in Engineering (Electronics and Communication) from R.K.G.I.T. Ghaziabad, India, in 2009. Currently, her research interest includes low power and high speed memories at nanoscale.



**Dr. Amit Chaudhry** was born in India, in 1976. He completed his Ph.D (Microelectronics) in 2010 from Panjab University, Chandigarh, India. He joined Panjab University, University Centre for instrumentation and microelectronics October, 2002. He was responsible for teaching and research in VLSI and microelectronics to post graduate students. His research areas include device modeling for sub 100nm MOSFETS. He is a life member of various societies in the area of microelectronics. Currently he is a Senior Assistant Professor, University Institute of Engineering and Technology, Panjab University, Chandigarh. He has more than 18 publications in national and international Journals/Conference proceedings.



**Garima Joshi** was born in India, in 1981. She is M.E (Electronics and Communication Engineering) from UIET, Panjab University, Chandigarh, India. Her area of research includes modeling and simulation of Nanoscale MOSFETS. Currently she is working as Assistant Professor (Electronics and Communication Engineering) in UIET, Panjab University, Chandigarh, India. She has 5 publications in international Journals/Conference proceedings.

